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length coding, word packing/unpacking, bit indexing, address ... a barrel shifter, if the data input signal is n-bits wide, then the ...

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Inventors Yuji Ishikawa, Yokohama (JP) Assignees Canon Kabushiki ...

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A barrel shifter rotates the input data bits by the amount specified by the resources usage by packing logic functions and common inputs together. ...

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The decompressor then shifts in more compressed data using a barrel shifter and concatenates them with the remaining compressed bits. ...

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Data arithmetic logic unit (Data ALU) with fully pipelined 24 × 24-bit parallel Multiplier-Accumulator (MAC),. 56-bit parallel barrel shifter (fast shift ...

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Data packing of the line buffer (i.e. the RAM) can be performed on a multiple ... Figures 20 - 22 illustrate the modules constituting the barrel shifter; ...

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offer excellent packaging and communication advantages, a soft- a barrel shifter to enable the bs and bsi instructions. ...

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At a data packing density of 1.3 bits per mm, this implies that over 53 mm a ring or rotating barrel shifter, and rotate them by 35 bits (in steps of ...

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BREG: barrel shifter, packing, unpacking and clipping operators ...

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typically utilize barrel shifter circuitry to allow a plurality of bytes to be shifted by reordered and shifted to perform data packing and unpacking. ...

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20-Mbytes/sec asynchronous transfer rate at each port for maximum data ... Single-cycle barrel shifter for 0-31 single-cycle right or left shifts for fast ...

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Only load, store, and swap instructions can access data from memory. ... The Shift stage contains the full barrel shifter. All shifts, including those ...

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The barrel shifter can shift data up to 16 bits left or right in a single ... packaging and electrical characteristics, the device datasheets listed here ...

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This capability allows programmers to tightly pack data or code and conserve RAM, which is a high-cost item for embedded applications. ...

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